

FIG. 2a

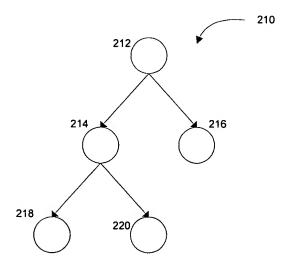
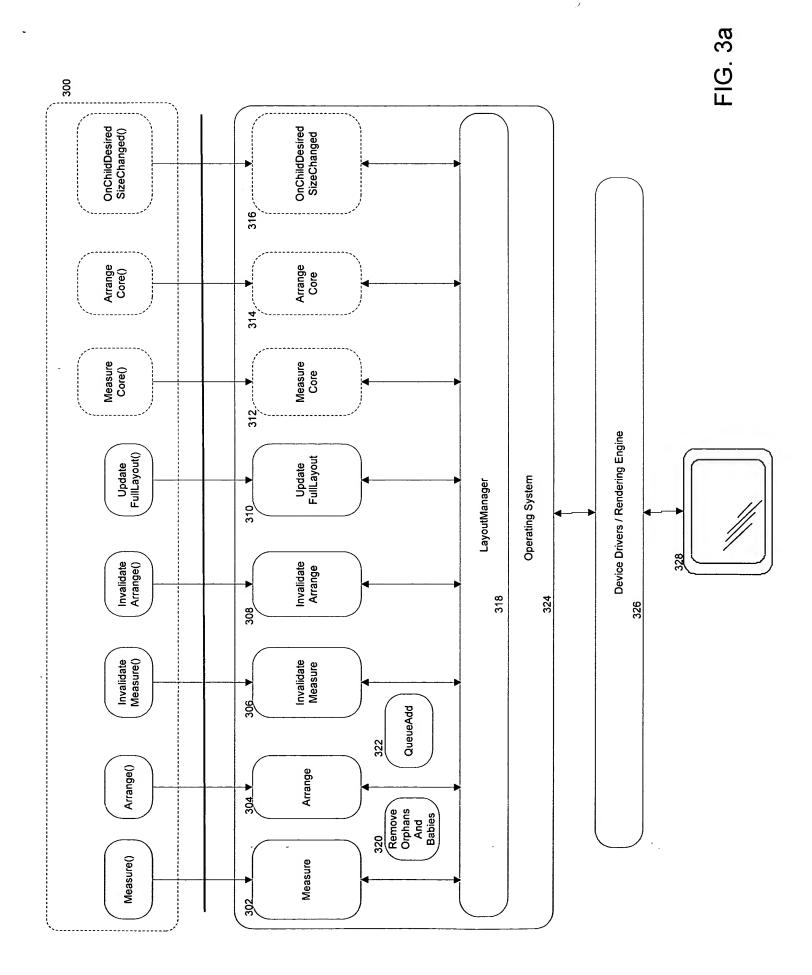


FIG. 2b



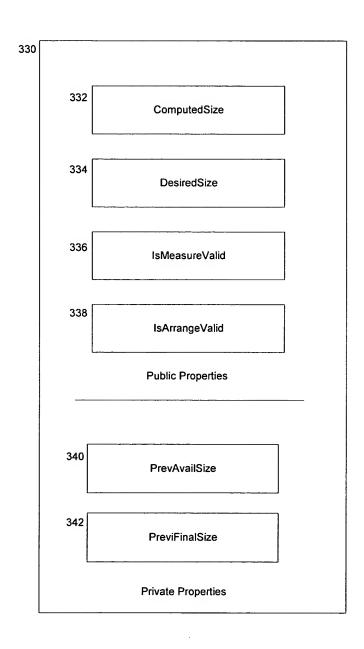


FIG. 3b

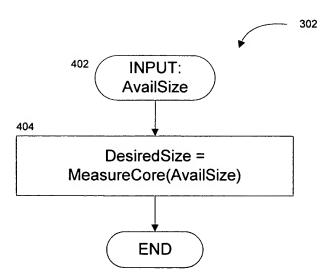


FIG. 4

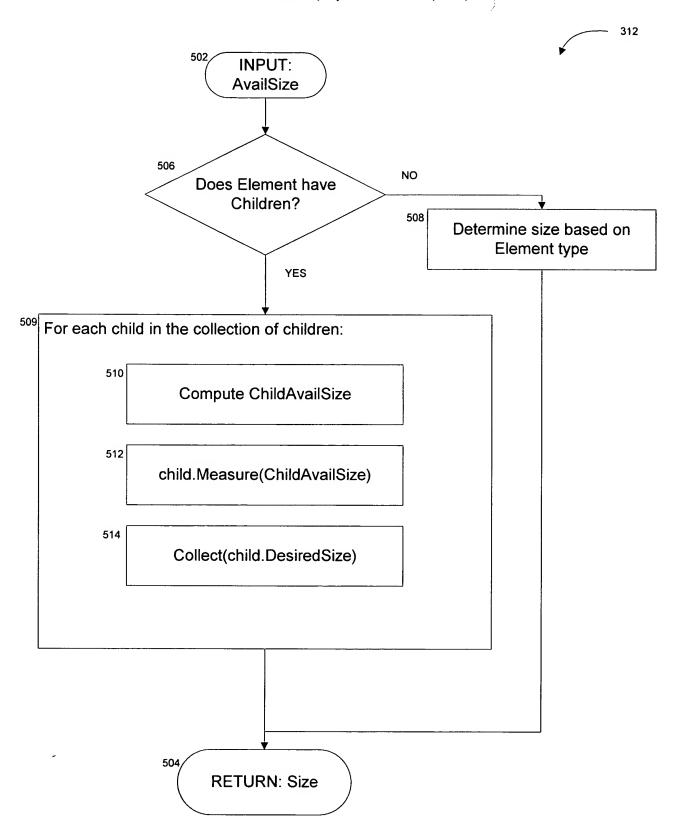


FIG. 5

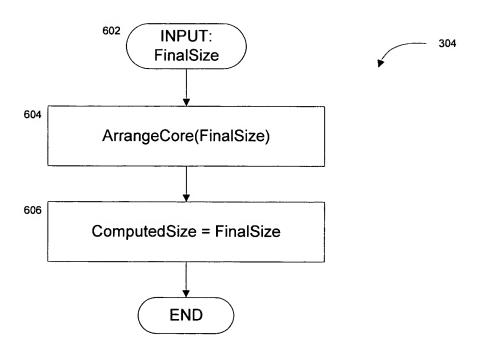


FIG. 6

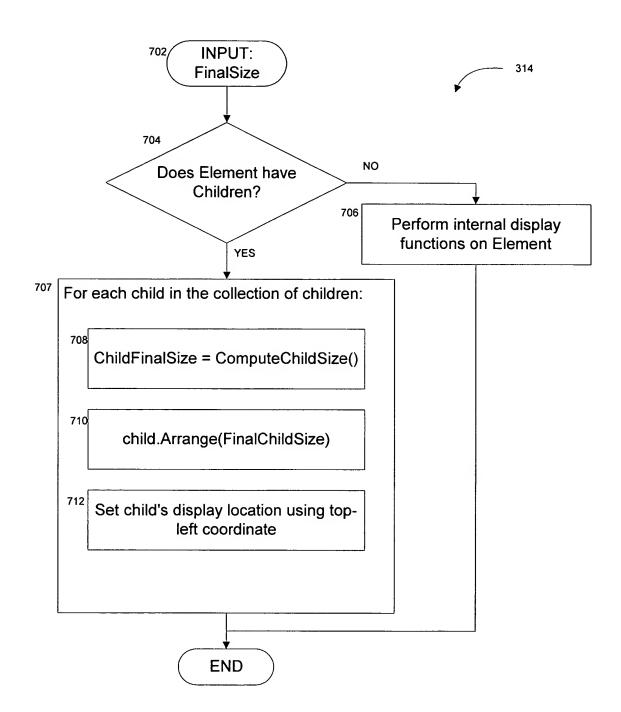


FIG. 7

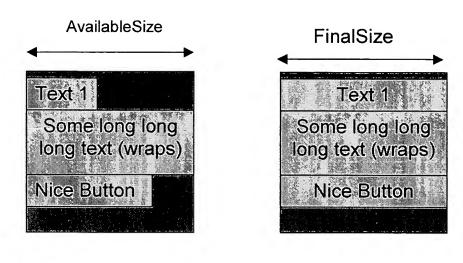


FIG. 8b

FIG. 8a

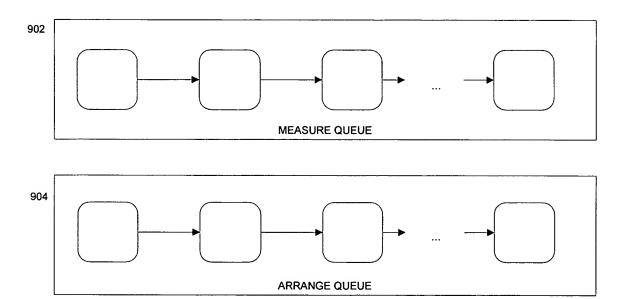
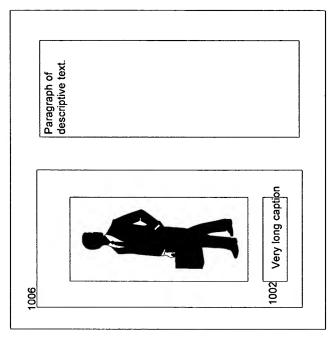
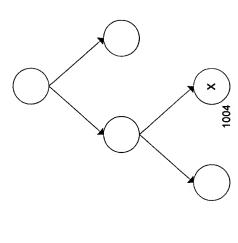
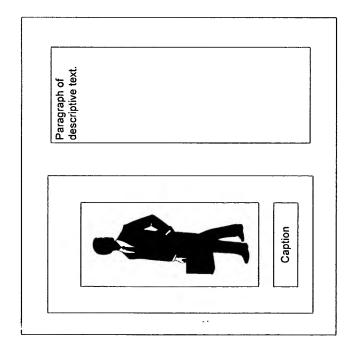


FIG. 9







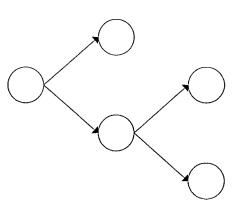


FIG. 10a

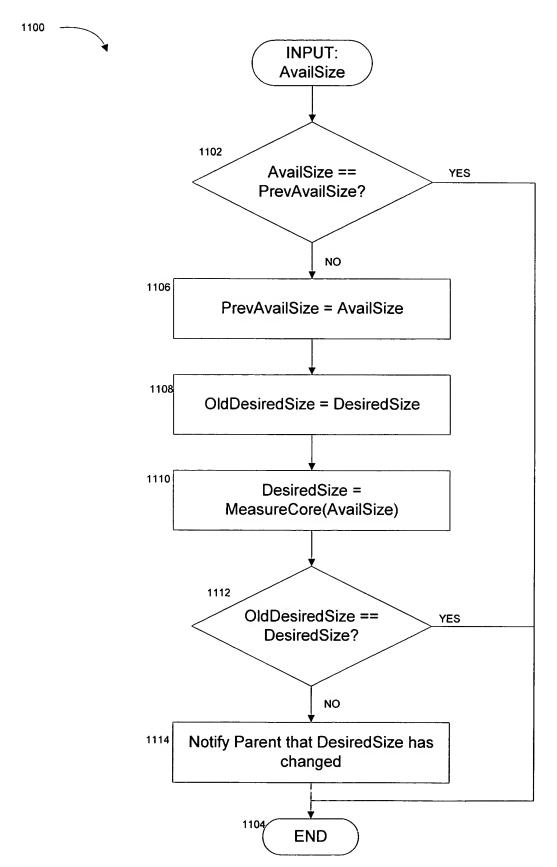


FIG. 11

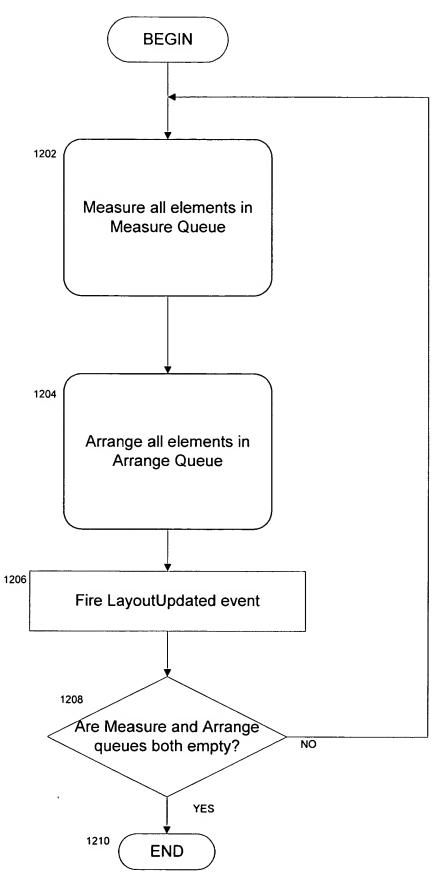


FIG. 12

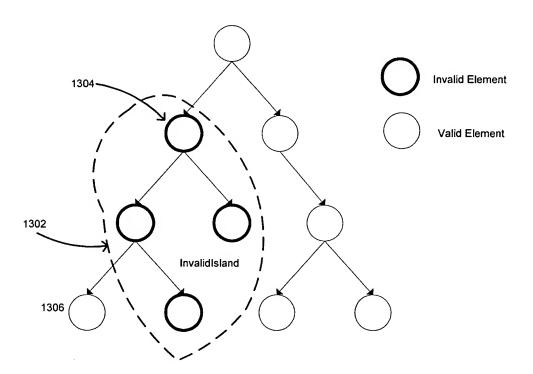
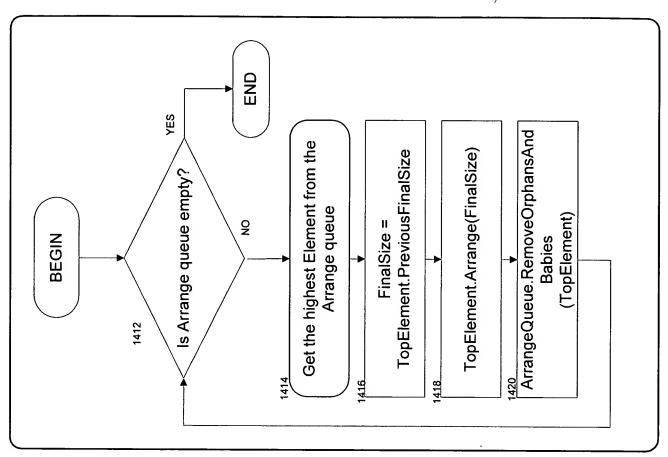


FIG. 13



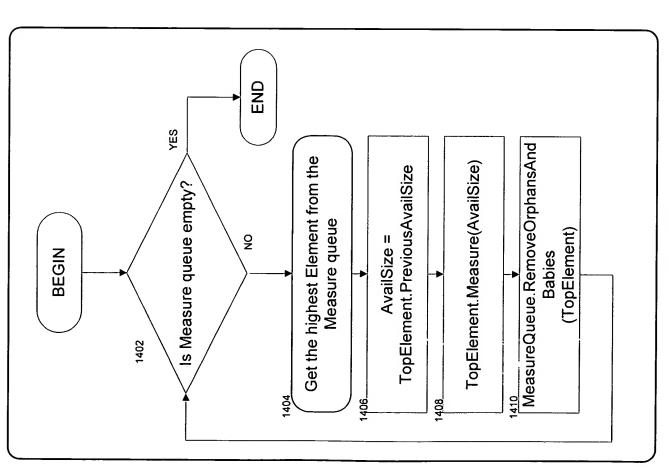
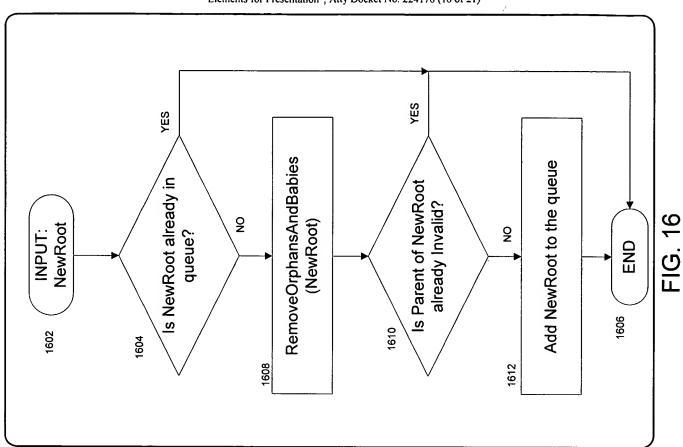
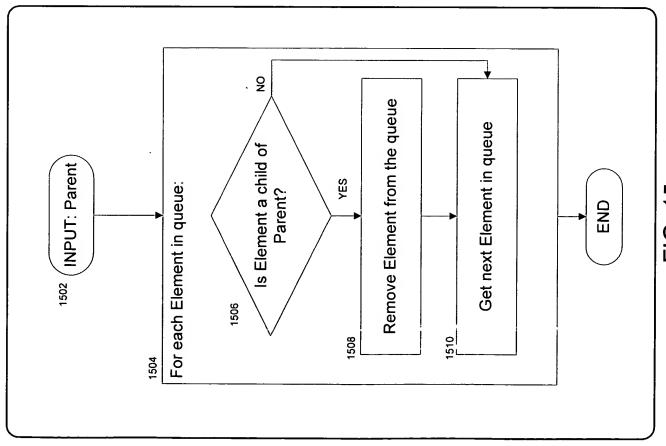


FIG. 14b

FIG. 14a





G. 15

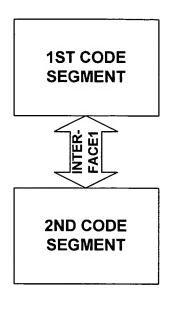


FIGURE A1

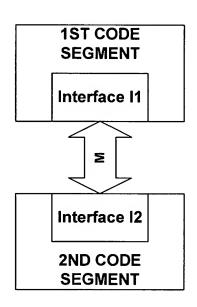


FIGURE A2

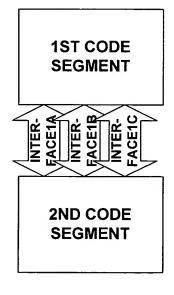


FIGURE B1

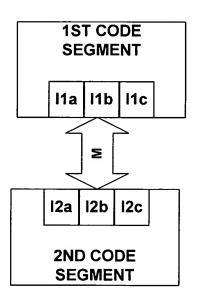
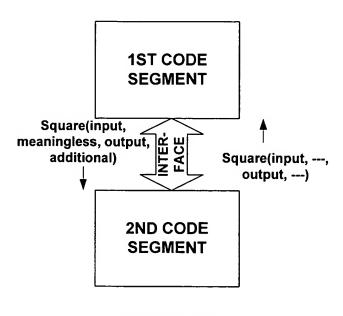


FIGURE B2



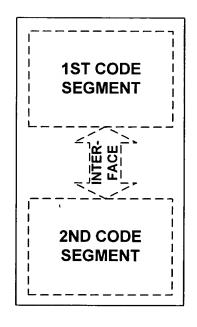
Interface I1'

Interface I2'

2ND CODE
SEGMENT

FIGURE C1

FIGURE C2



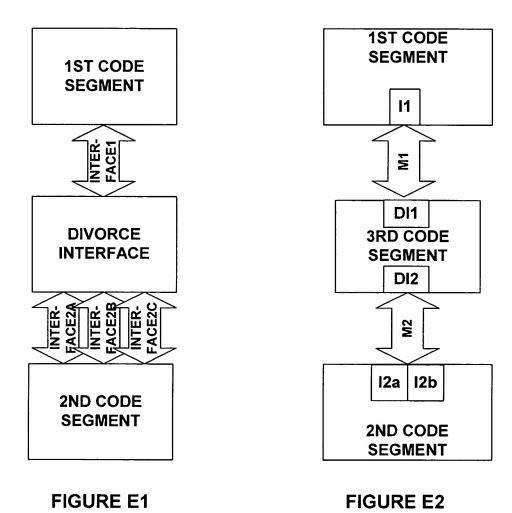
Interface I2A

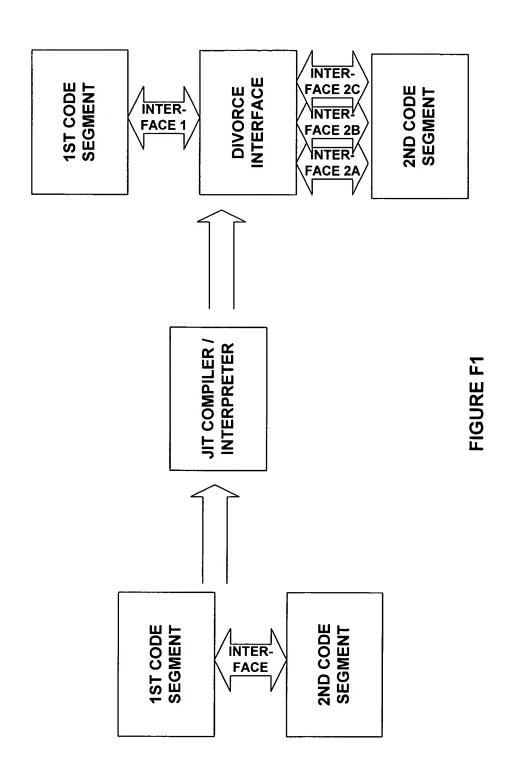
Interface I2B

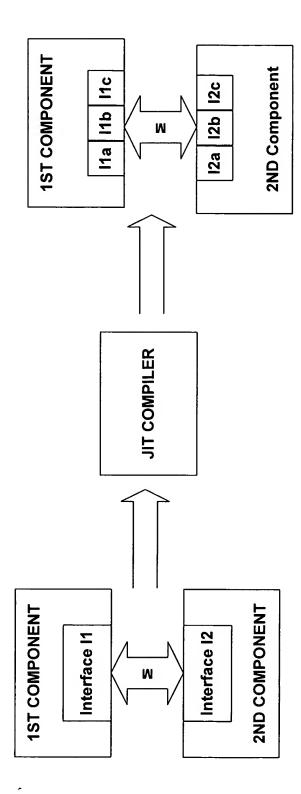
2ND CODE
SEGMENT

FIGURE D1

FIGURE D2







GURE F2